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Docket No.: 386168016US  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Curtis B. Robinson Jr.

Application No.: 10/626,994

Confirmation No.: 4169

Filed: July 24, 2003

Art Unit: 2838

For: VOLTAGE REGULATOR USING A  
TRANSIMPEDANCE BLOCK TO  
MEASURE OUTPUT

Examiner: Not Yet Assigned

**INFORMATION DISCLOSURE STATEMENT (IDS)**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is filed before the mailing date of a first Office Action on the merits as far as is known to the undersigned (37 CFR 1.97(b)(3)).

A copy of each reference on the PTO/SB/08 is attached.

The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this

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application by this firm) to our Deposit Account No. 50-0665, under Order No. 386168016US.

Dated: July 28, 2004

Respectfully submitted,

By 

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PERKINS COIE LLP

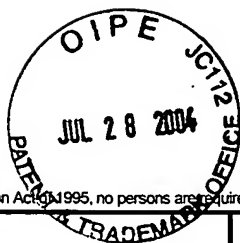
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Substitute for form 1449A/B/PTO				<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <i>(Use as many sheets as necessary)</i>				Application Number	10/626,994-Conf. #4169
				Filing Date	July 24, 2003
				First Named Inventor	Curtis B. Robinson Jr.
				Art Unit	2838
				Examiner Name	Not Yet Assigned
Sheet	1	of	1	Attorney Docket Number	386168016US

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number Number-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>

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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	CA	Author unknown, "LT3420-Photoflash Capacitor Charger with Automatic Refresh," Linear Technology Corporation, 2002, Milpitas, California, US, (16 pages).	

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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Examiner Signature		Date Considered	
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